Dial g	DataS	tar			•
options	logoff	feedback	help		Sciencifo
				databases easy search	
			A	dvanced Search:	
			Ins	pec - 1898 to date (INZZ)	
				limit ,	

Search history:

	i ilistory.				
No.	Database	Search term	Info added since	Results	
СР		[Clipboard]		0	-
1	INZZ	memory SAME output\$3 SAME tim\$3 SAME (mode OR standard OR type) SAME switch\$3 SAME latch\$3 SAME register	unrestricted	0	-
2	INZZ	memory AND output\$3 AND tim\$3 AND (mode OR standard OR type) AND switch\$3 AND latch\$3 AND register	unrestricted	0	-
3	INZZ	(select\$3 OR arbit\$3 OR arbitrat\$3) AND (buffer OR queue OR cache)	unrestricted	4734	show titles
4	INZZ	memory AND (mode OR standard OR type) AND time NEAR switch\$3	unrestricted	222	show titles
5	INZZ	3 AND 4	unrestricted	5	show titles
6	INZZ	flash OR ic OR integrated ADJ circuit NEAR card OR (non-volatile OR non ADJ volatile) WITH memory	unrestricted	86540	show titles
7	INZZ	4 AND 6	unrestricted	11	show titles

hide | delete all search steps... | delete individual search steps...

Select special search terms from the following list(s):

Publication year 1950-

Enter your search term(s): <u>Search tip</u>	s 🗌 Thesaur	us mapping whole document	
Information added since: (YYYYMMDD)	or: none		search
☐ Images			



Home | Login | Logout | Access Information | Alerts | Purchase History | ** Cart |

Welcome United States Patent and Trademark Office

BROWSE

SEARCH

IEEE XPLORE GUIDE

□Search Session History

Wed, 14 Nov 2007, 10:23:52 PM EST

Search Query Display

Edit an existing query or compose a new query in the Search Query Display.

Select a search number (#)

- Add a query to the Search Query Display
- Combine search queries using AND, OR, or NOT
- Delete a search
- Run a search

Recent Search Queries			
<u>#1</u>	((memory and output and time and (mode or standard or type) and switch and latch and register) <in>metadata)</in>		
#2	((memory and output and time and (mode or standard or type) and switch and latch and register) <in>metadata)</in>		
<u>#3</u>	((memory and output and (mode or standard or type) and switch and latch) <in>metadata)</in>		
<u>#4</u>	((memory and output and (mode or standard or type)) <in>metadata)</in>		
<u>#5</u>	((((memory and output and (mode or standard or type)) <in>metadata))<and>(switch<in>metadata))</in></and></in>		
<u>#6</u>	(((((((memory and output and (mode or standard or type)) <in>metadata))<and>(switch<in>metadata)))<and>(timing and switch<in>metadata))</in></and></in></and></in>		
<u>#7</u>	((((((((memory and output and (mode or standard or type)) <in>metadata))<and>(switch<in>metadata)))<and>(timing and switch<in>metadata)))<and>((hs-mmc or hsmmc or (high speed multimedia card) or (multimedia card)) and (sd or secure</and></in></and></in></and></in>		

and digital)<in>metadata))

(((((((((memory and output and (mode or standard or type))
<in>metadata))<and>(switch<in>metadata)))<and>(timing and
switch<in>metadata)))<AND>((timing or clock and cycle)
<in>metadata))

((((((((((memory and output and (mode or standard or type))
<in>metadata))<and>(switch<in>metadata)))<and>(timing and
switch<in>metadata)))<and>((timing or clock and cycle)
<in>metadata)))<AND>(operation and (mode or standard)
<in>metadata))

Help Contact Us Privacy &:

© Copyright 2007 IEEE –

indexed by Inspec*